

wherein each of the plurality of pixels comprises a sensor portion and a light emitting element portion,

wherein the output switching circuit comprises a first logical circuit and a second logical circuit,

wherein a first signal line is connected to one of the first logical circuit and second logical circuit, and a second signal line is connected to the other,

wherein the signal line driver circuit outputs a timing signal to the first logical circuit and to the second logical circuit,

wherein one of the first logical circuit and the second logical circuit outputs a first signal to the first signal line, and the other outputs a second signal to the second signal line, and

wherein the first signal is different from the second signal.

6. (Amended) A semiconductor device comprising:

a pixel portion comprising a plurality of pixels;

a signal line driver circuit; and

an output switching circuit,

wherein each of the plurality of pixels comprises a sensor portion and a liquid crystal element portion,

wherein the output switching circuit comprises a first logical circuit and a second logical circuit,

wherein a first signal line is connected to one of the first logical circuit and second logical circuit, and a second signal line is connected to the other,

wherein the signal line driver circuit outputs a timing signal to the first logical circuit and to the second logical circuit,

wherein one of the first logical circuit and the second logical circuit outputs a first signal to the first signal line, and the other outputs a second signal to the second signal line, and

wherein the first signal is different from the second signal.

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